

10072145
02/07/02
10072145
02/07/02

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10072145	FILING DATE 02/07/2002	CLASS 257	SUBCLASS 501	GAU 2811	EXAMINER <i>Magee</i>
----------------------	---------------------------	--------------	-----------------	-------------	--------------------------

**APPLICANTS: Averett Guy; Kamekona Keith; Sudhama Chandrasekhara; Cai Weizhong; Bratten Gordon; Ruiz Bladimiro;

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiner's initials		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO ONS00317
TITLE : Semiconductor device and method of providing regions of low substrate capacitance U.S.DEP'T. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED					
		<table border="1"> <tr> <td>Total Claims</td> <td>Print Claim for O.G</td> </tr> </table>			Total Claims	Print Claim for O.G	
Total Claims	Print Claim for O.G						
Assistant Examiner		DRAWING					
		<table border="1"> <tr> <td>Sheets Drwg.</td> <td>Figs.Drwg.</td> <td>Print Fig.</td> </tr> </table>			Sheets Drwg.	Figs.Drwg.	Print Fig.
Sheets Drwg.	Figs.Drwg.	Print Fig.					
Primary Examiner							
PREPARED FOR ISSUE		Application Examiner					
TERMINAL DISCLAIMER		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.					

FILED WITH:

DISK (CRF)

CD-ROM
(Attached in pocket on right inside flap)